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By: 

Date: July 18, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernhard Schätzler et al.
Applic. No. : 09/688,465
Filed : October 16, 2000
Title : Electronic Component With An Integrated Circuit Mounted On An Island Of A Lead Frame
Examiner : Nitin Parekh - Art Unit: 2811

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications which were cited in foreign patent proceedings are submitted herewith:

United States Patent No. 5,191,403 (Nakazawa), dated March 2, 1993;

German Published, Non-Prosecuted Patent Application DE 36 35 375 A1 (Ersoy), dated April 28, 1988, system carrier for electronic components;

German Published, Non-Prosecuted Patent Application DE 40 41 346 A1 (Pape), dated June 25, 1992;

German Published, Non-Prosecuted Patent Application DE 195 26 010 A1 (Schätzler et al.), dated January 23, 1997, electronic component and method for producing an electronic component;

European Patent Application EP 0 261 324 A1 (Bednarz et al.), dated March 30, 1988,

Japanese Patent Abstract JP 60 119 756 (Kitamura), dated June 27, 1985;

Japanese Patent Abstract JP 60 189 956 (Okatome et al.), dated September 27, 1985;

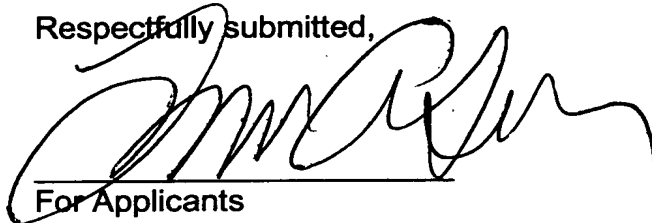
Japanese Patent Abstract JP 61 032 435 (Okihara), dated February 15, 1986;

Japanese Patent Abstract JP 04 101 437 (Aida), dated April 2, 1992;

Japanese Patent Abstract JP 07 038 040 (Ito), dated February 7, 1995.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

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